

Appl. No. 10/710,264
Amdt. dated August 29, 2006
Reply to Office action of July 21, 2006

Abstract of Disclosure

A circuit and a method for aligning transmitted data by adjusting transmission
~~transmitting~~ timing of for a plurality of lanes. The method includes utilizing different
5 initial values to reset a count value corresponding to a lane when a plurality of COM
symbols are detected on the lane, utilizing an increment value to increase the count value
corresponding to the lane when a COM symbol is not detected on the lane, and utilizing a
plurality of count values corresponding to the lanes to align transmitted data ~~transmitting-~~
10 ~~timing~~ of the lanes when a COM symbol is not detected on the lanes within a
predetermined period of time.